

3.2x1.6mm PHOTOTRANSISTOR

Part Number: KP-3216P3C

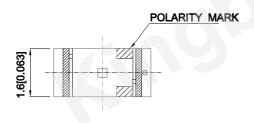
Features

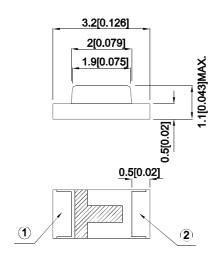
- 3.2mmx1.6mm SMD LED, 1.1mm thickness.
- Mechanically and spectrally matched to infrared emitting LED lamp.
- Package : 2000pcs / reel.
- Moisture sensitivity level : level 3.
- RoHS compliant.

Description

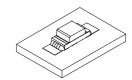
Made with NPN silicon phototransistor chips.

Package Dimensions









- 1. All dimensions are in millimeters (inches).
- 2. Tolerance is ±0.2(0.0079") unless otherwise noted.
- 3. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.

 4. The device has a single mounting surface. The device must be mounted according to the specifications.

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Electrical / Optical Characteristics at TA=25°C

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
VBR CEO	Collector-to-Emitter Breakdown Voltage	30			V	Ic=100uA Ee=0mW/c m²
VBR ECO	Emitter-to-Collector Breakdown Voltage	5			V	IE=100uA Ee=0mW/c m²
VCE (SAT)	Collector-to-Emitter Saturation Voltage			0.8	V	Ic=2mA Ee=20mW/c m³
I CEO	Collector Dark Current			100	nA	VcE=10V Ee=0mW/c m²
TR	Rise Time (10% to 90%)		15		us	VcE = 5V Ic=1mA RL=1000Ω
TF	Fall Time (90% to 10%)		15		us	
I (ON)	On State Collector Current	0.2	0.4		mA	VcE = 5V Ee=1mW/c m ² λ=940nm

Absolute Maximum Ratings at TA=25°C

Parameter	Max.Ratings		
Collector-to-Emitter Voltage	30V		
Emitter-to-Collector Voltage	5V		
Power Dissipation at (or below) 25°C Free Air Temperature	100mW		
Operating Temperature	-40°C To +85°C		
Storage Temperature	-40°C To +85°C		

Typical Electro-Optical Characteristics Curves Fig.1 Collector Power Dissipation vs.

Ambient Temperature Collector Power Dissipation Pd(mW) 125 100 75 50 50 25 7585 100

Ambient Temperature TA(°C)

Fig.2 Spectral Sensitivity vs. Wavelength 1.0 Ta=25°C 8.0 Relative Spectral Sensitivity 0.6 0.4 0.2 350 550 750 nm 1150 Wavelength & (nm)

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Note:
1. Relative humidity levels maintained between 40% and 60% in production area are recommended to avoid the build-up of static electricity – Ref JEDEC/JESD625-A and JEDEC/J-STD-033.

Fig.3 Relative Collector Current vs.
Ambient Temperature
_ 160

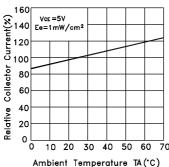


Fig.4 Collector Current Ic=f(Ee),Vce=5V, Ta=25°C

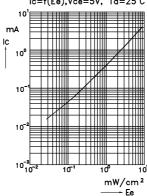


Fig.5 Collector Dark Current vs. Ambient Temperature

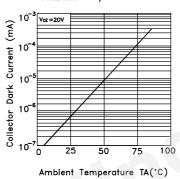


Fig.6 Collector Current vs.
Collector-Emitter Voltage

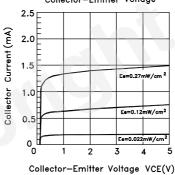
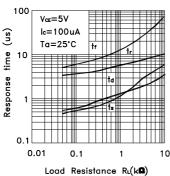
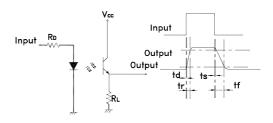


Fig.7 Response Time vs. Load Resistance



Test Circuit for Response Time

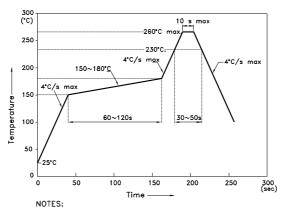


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KP-3216P3C

Reflow soldering is recommended and the soldering profile is shown below. Other soldering methods are not recommended as they might cause damage to the product.

Reflow Soldering Profile For Lead-free SMT Process.



- NOTES:

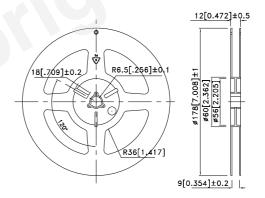
 1.We recommend the reflow temperature 245°C(+/-5°C).The maximum soldering temperature should be limited to 260°C. 2.Don't cause stress to the epoxy resin while it is exposed to high temperature.

 3.Number of reflow process shall be 2 times or less.

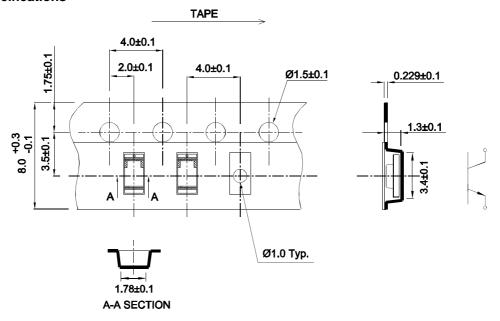
Recommended Soldering Pattern (Units: mm; Tolerance: ± 0.1)



Reel Dimension

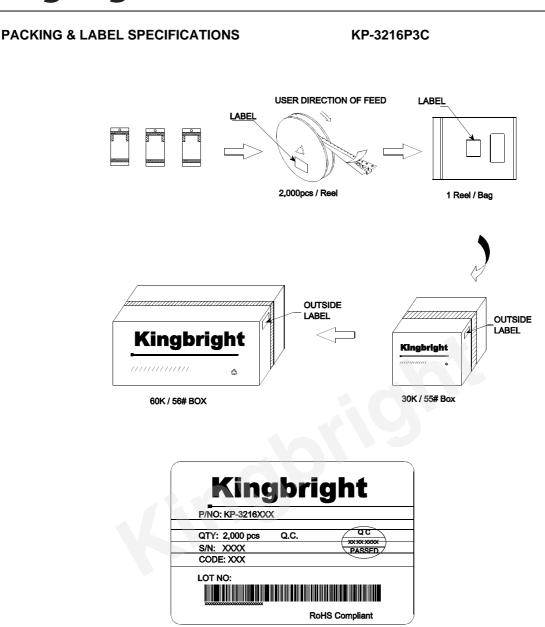


Tape Specifications



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